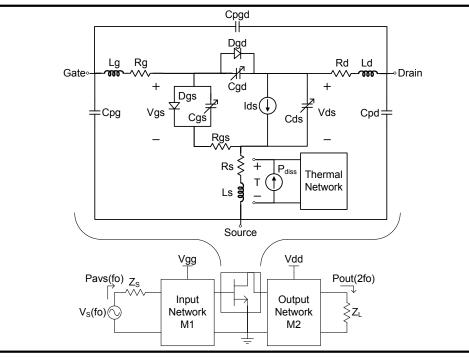


## High Power, High Conversion Gain Frequency Doublers Using SiC MESFETs and AIGaN/GaN HEMTs

Kelvin Yuk

University of California, Davis

- Development of high Pout, high CG frequency doublers
  - Combine frequency multiplication with power amplification and gain
  - Use of high power, wide bandgap devices
- Frequency doubler design using high-accuracy nonlinear models
  - Harmonic impedance pull sims for determining optimum Zload/Zsource
  - Realization using simple, low-loss, microstrip stub networks



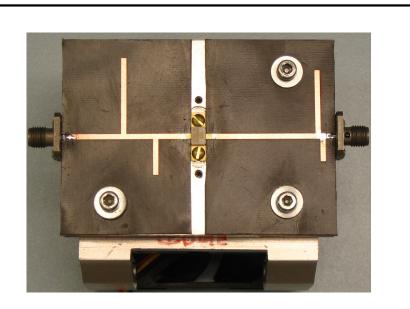


Photo of GaN HEMT-based frequency doubler.



WEPB: Frequency Conversion and Control



See us in Room 204ABC at 3:00pm on Wednesday for details...

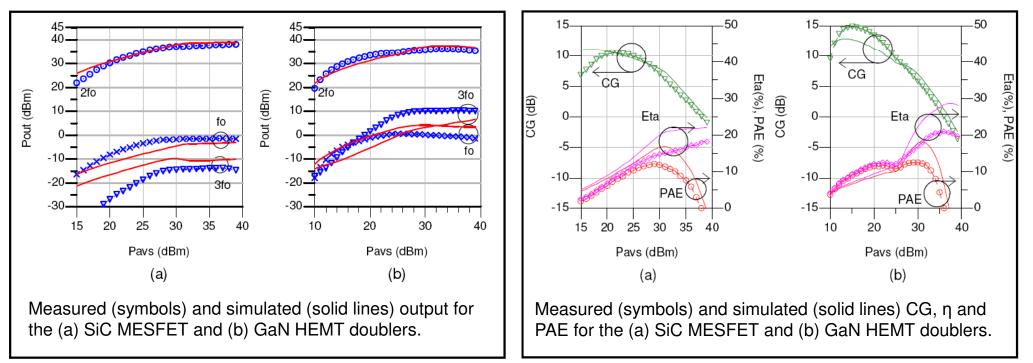


## High Power, High Conversion Gain Frequency Doublers Using SiC MESFETs and AIGaN/GaN HEMTs

Kelvin Yuk

University of California, Davis

- Highest power single-transistor frequency doublers to date
  - SiC MESFET-based doubler: CGmax=10.00dB, Poutmax=6.31W
  - AIGaN/GaN HEMT-based doubler: CGmax=14.80dB, Poutmax=4.14W
  - High suppression of unwanted first and third harmonics
  - Supports the use and development of high-accuracy nonlinear models
  - Cost-effective harmonic impedance pull simulations





**WEPB: Frequency Conversion and Control** 

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